

FIG. 1A
(Prior Art)

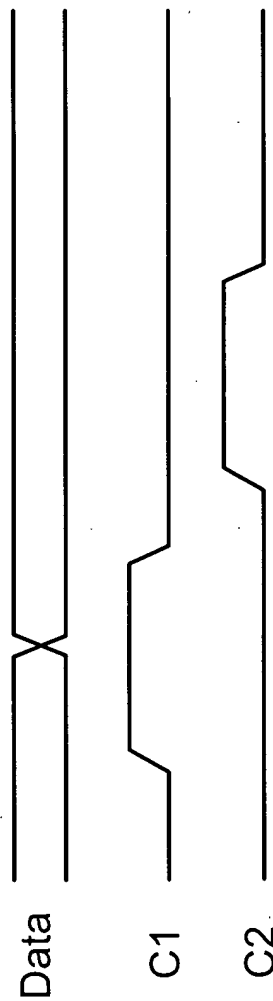


FIG. 1B
(Prior Art)

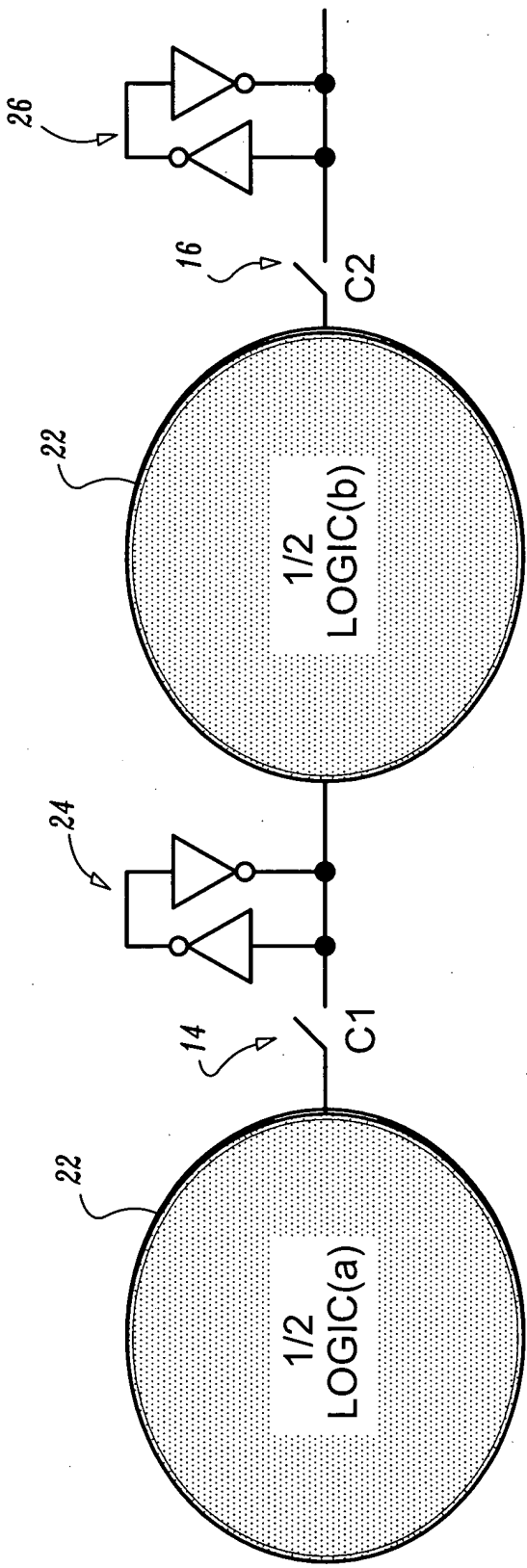
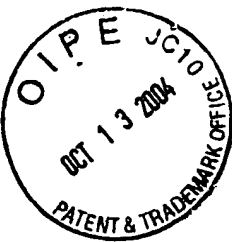


FIG. 2A
(Prior Art)

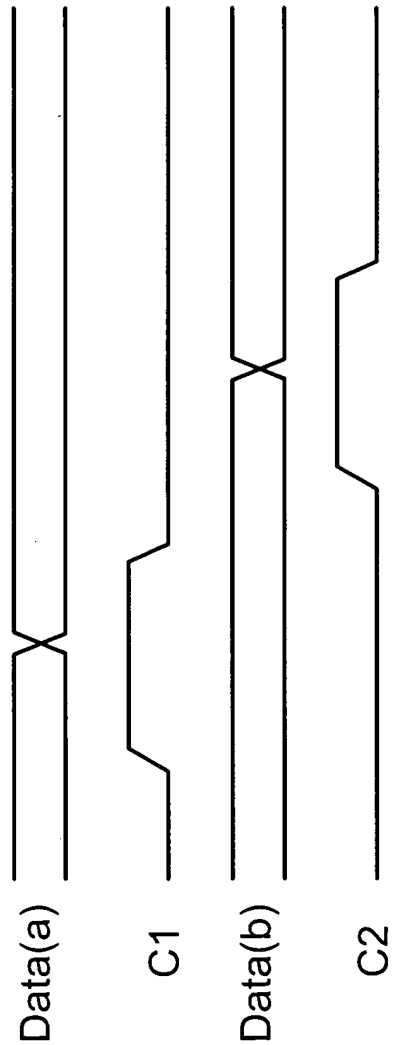


FIG. 2B
(Prior Art)

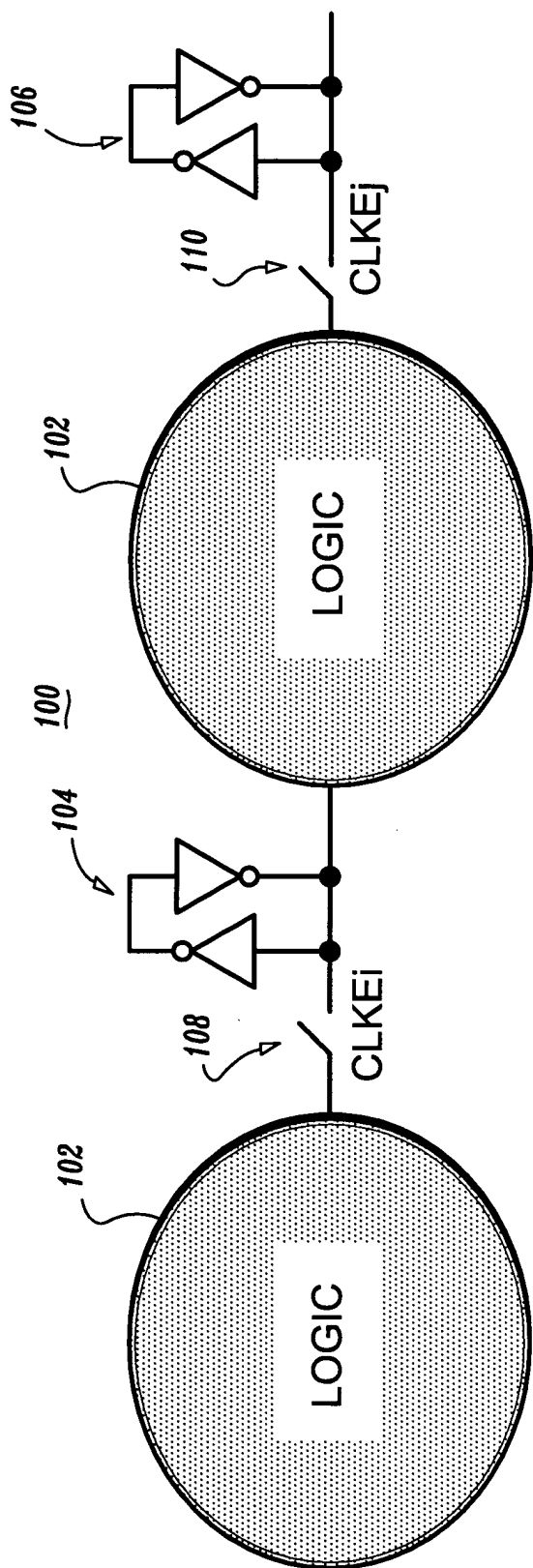
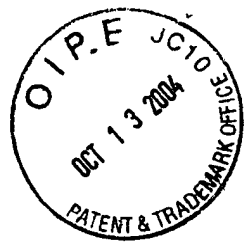


FIG. 3A

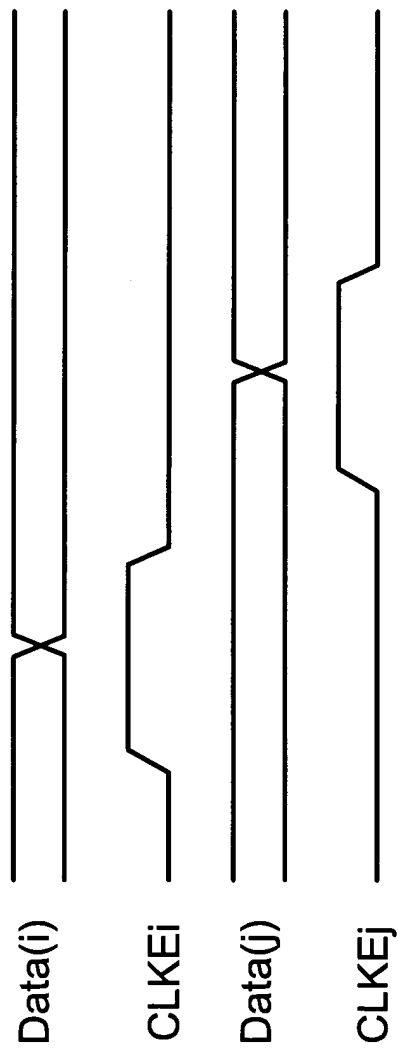
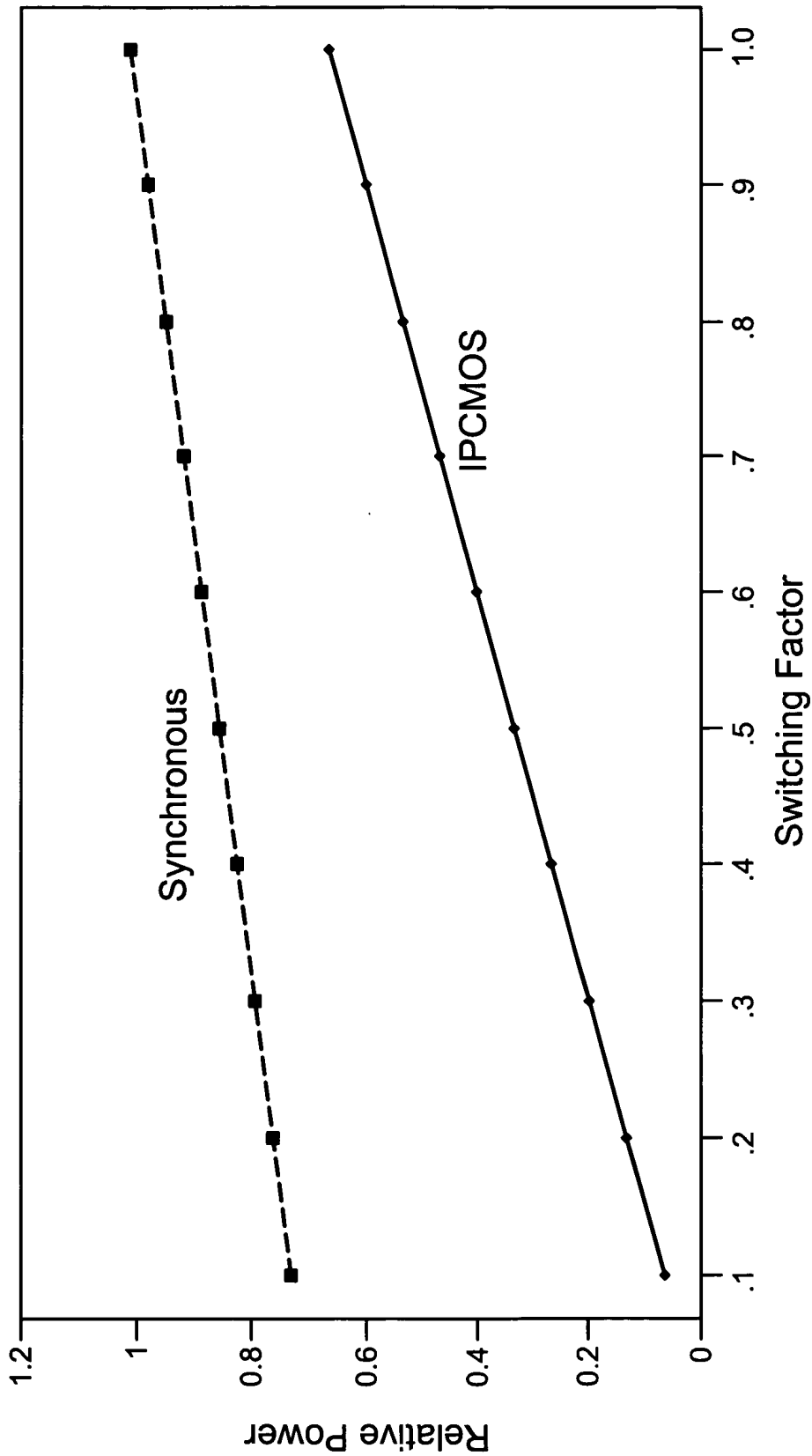
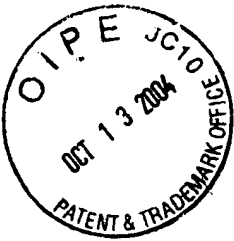


FIG. 3B



Power Versus Switching Factor

FIG. 4

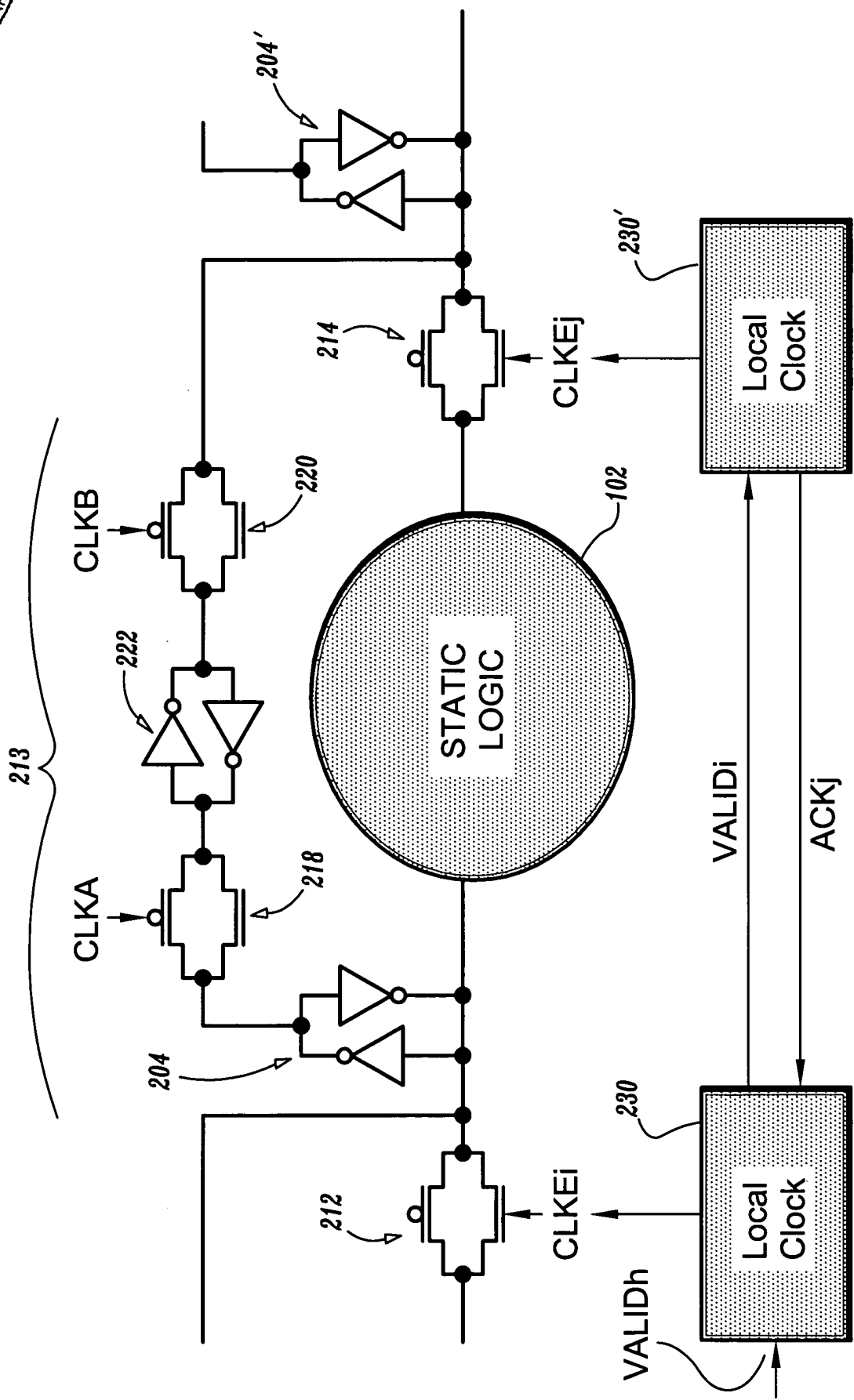
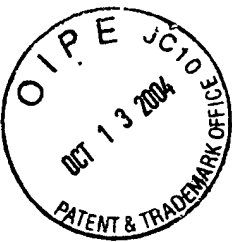


FIG. 5

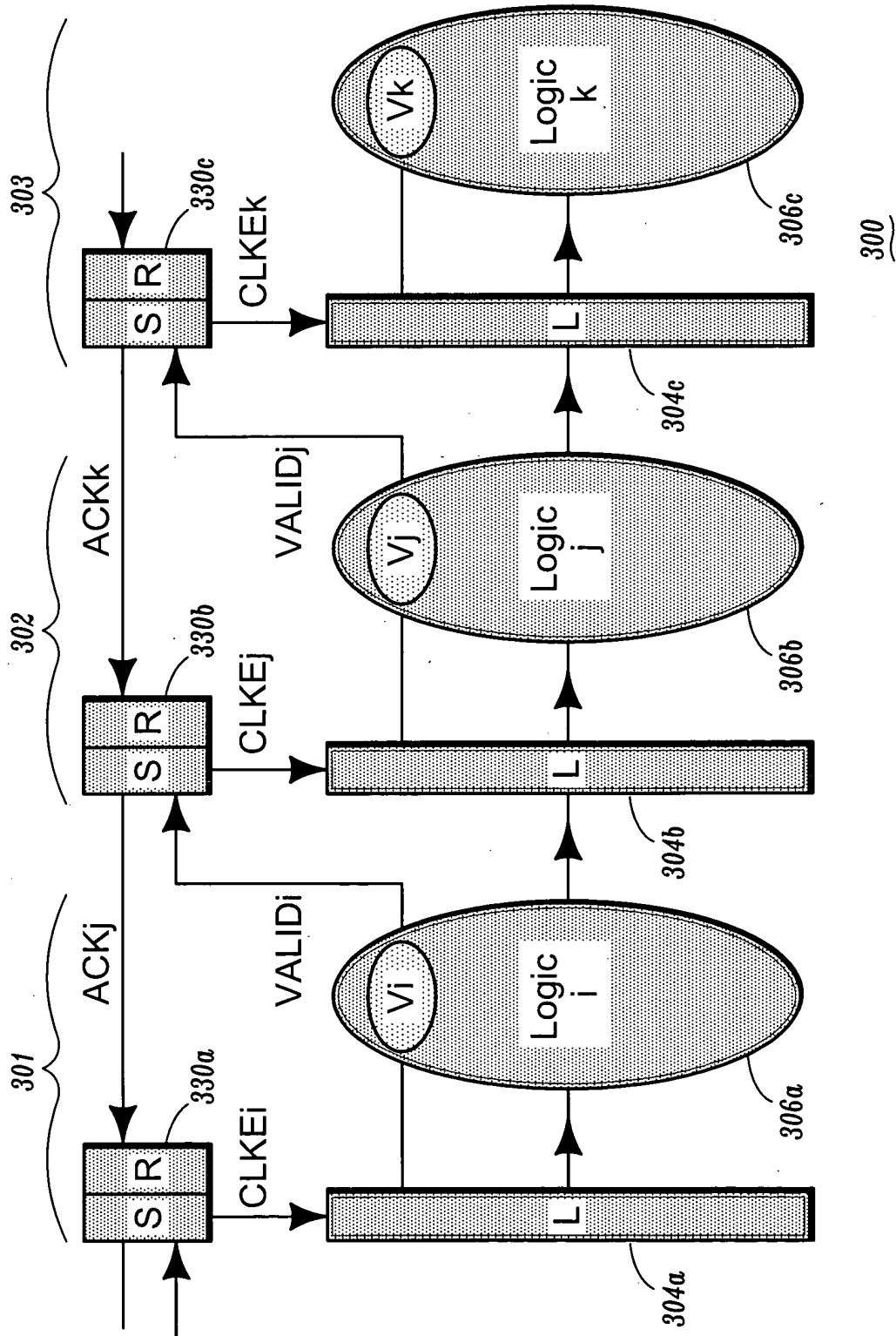


FIG. 6

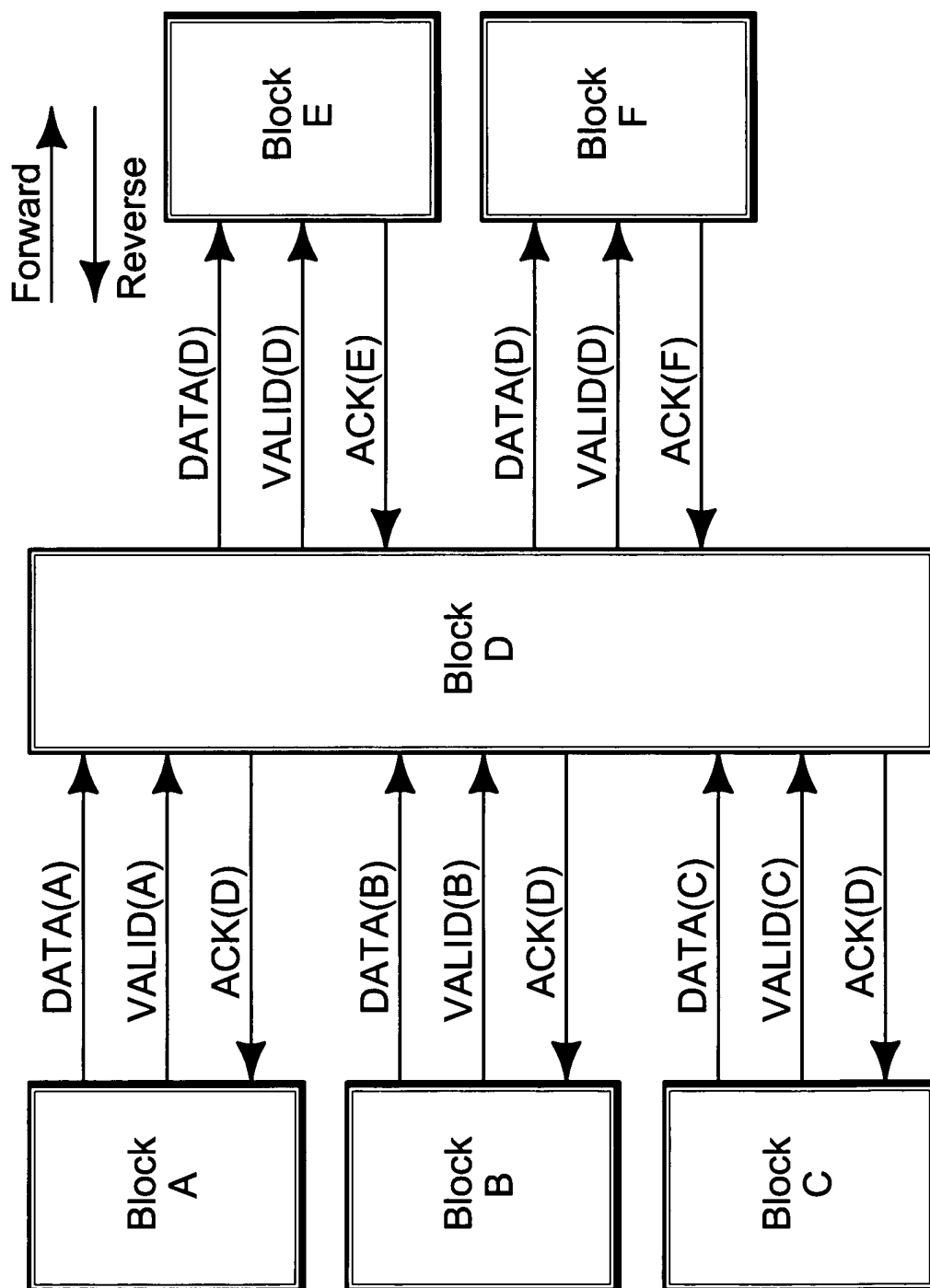


FIG. 7

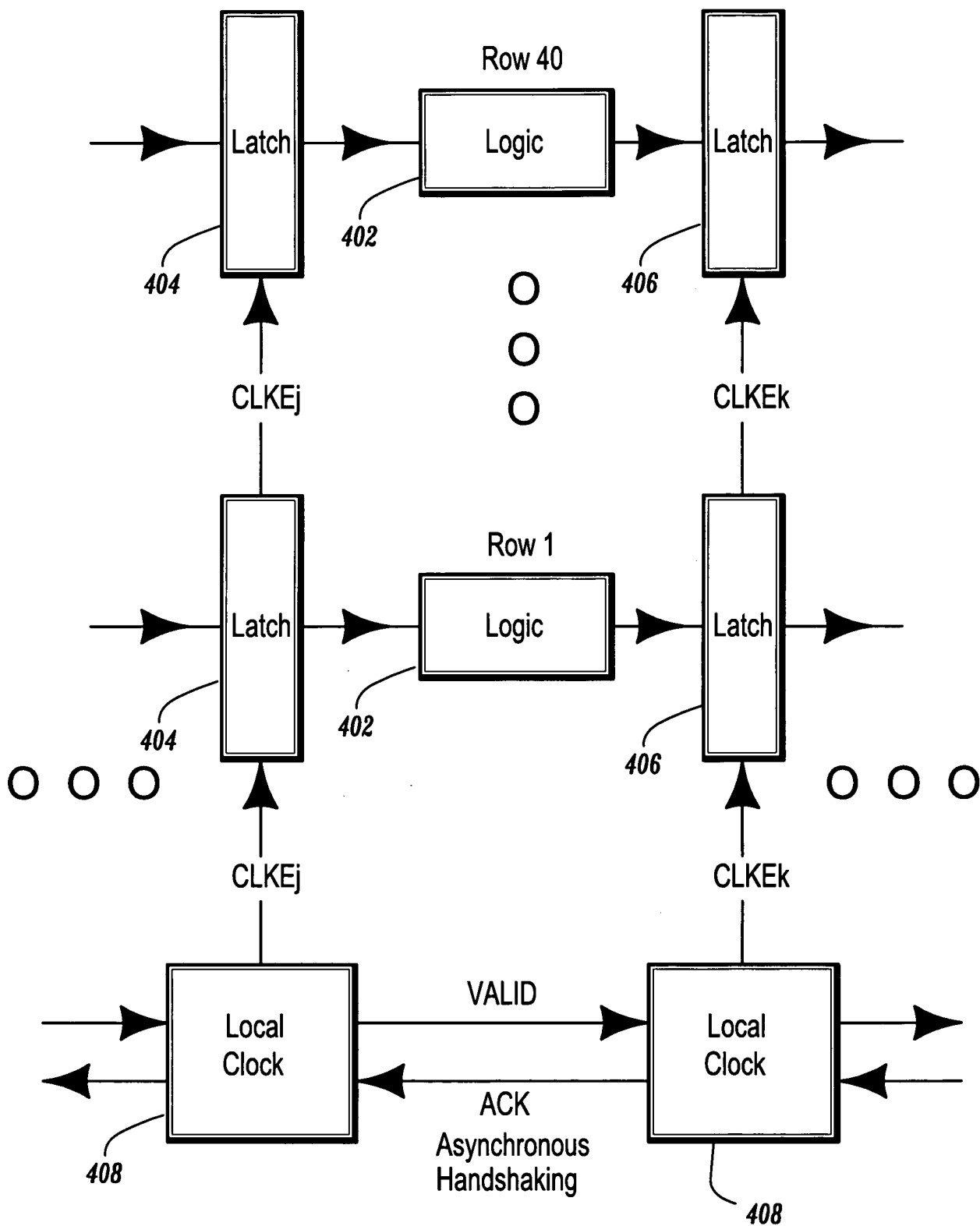
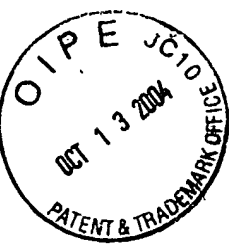


FIG. 8

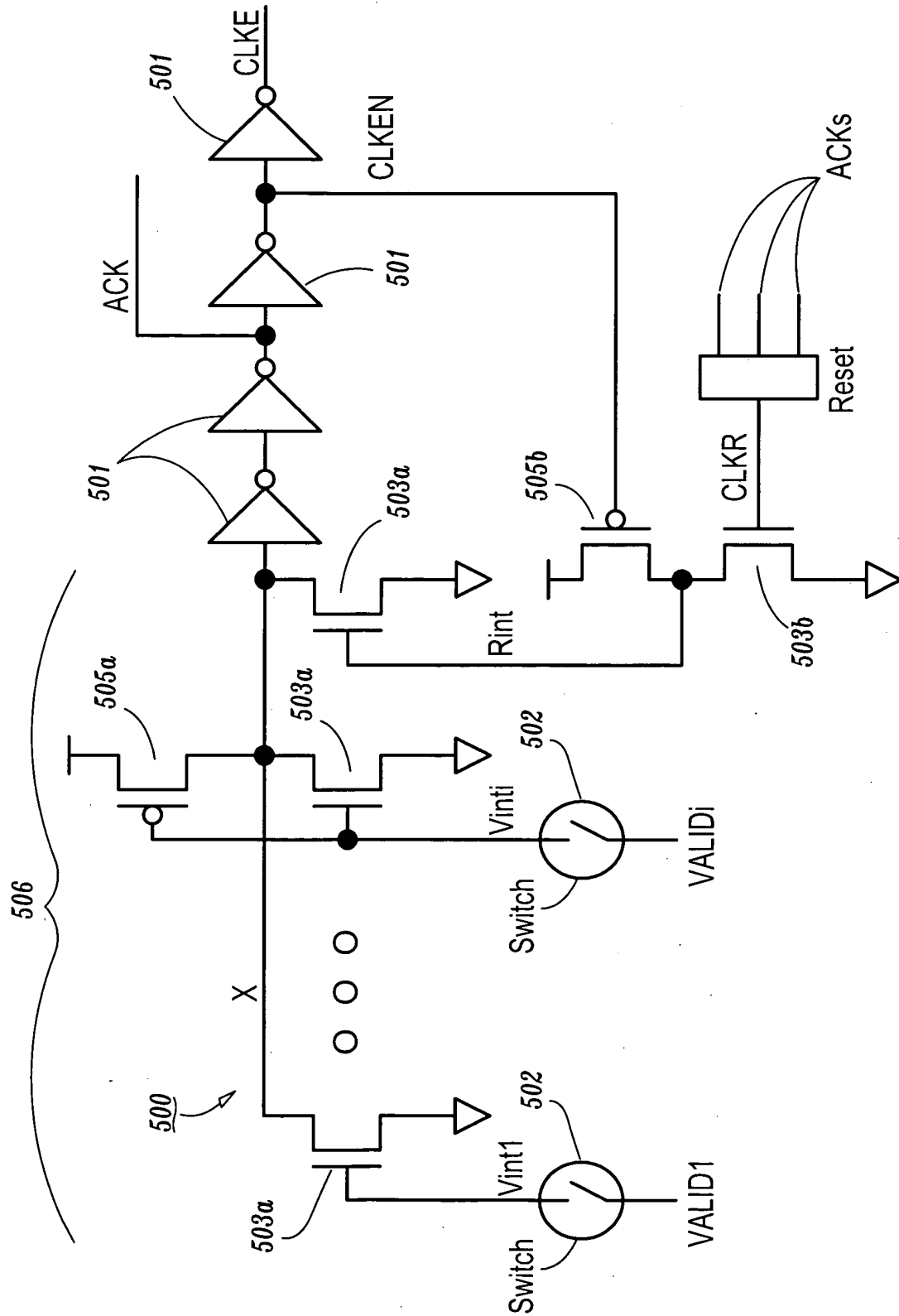
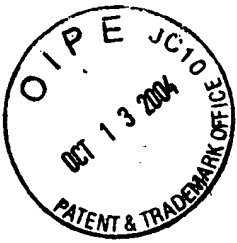


FIG. 9A

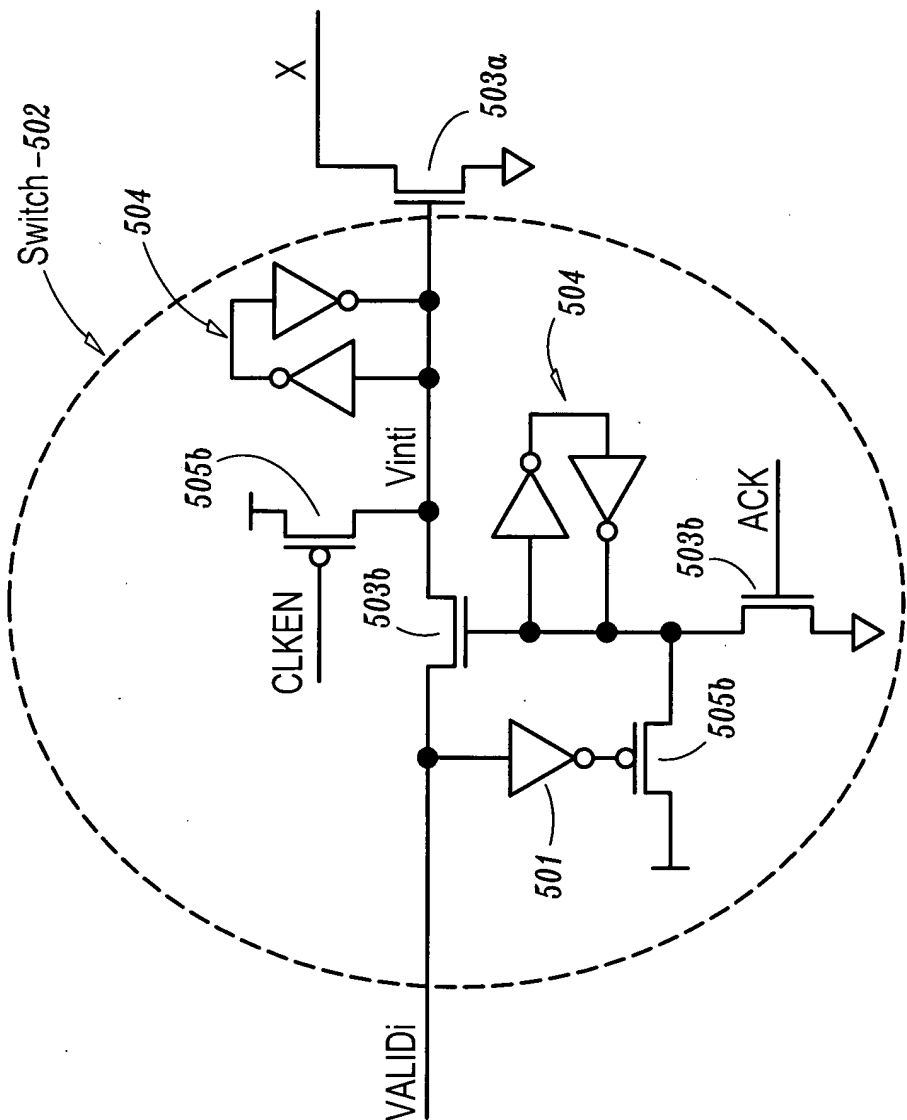
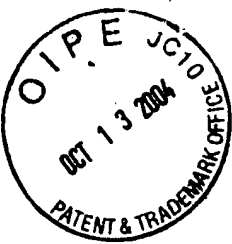


FIG. 9B

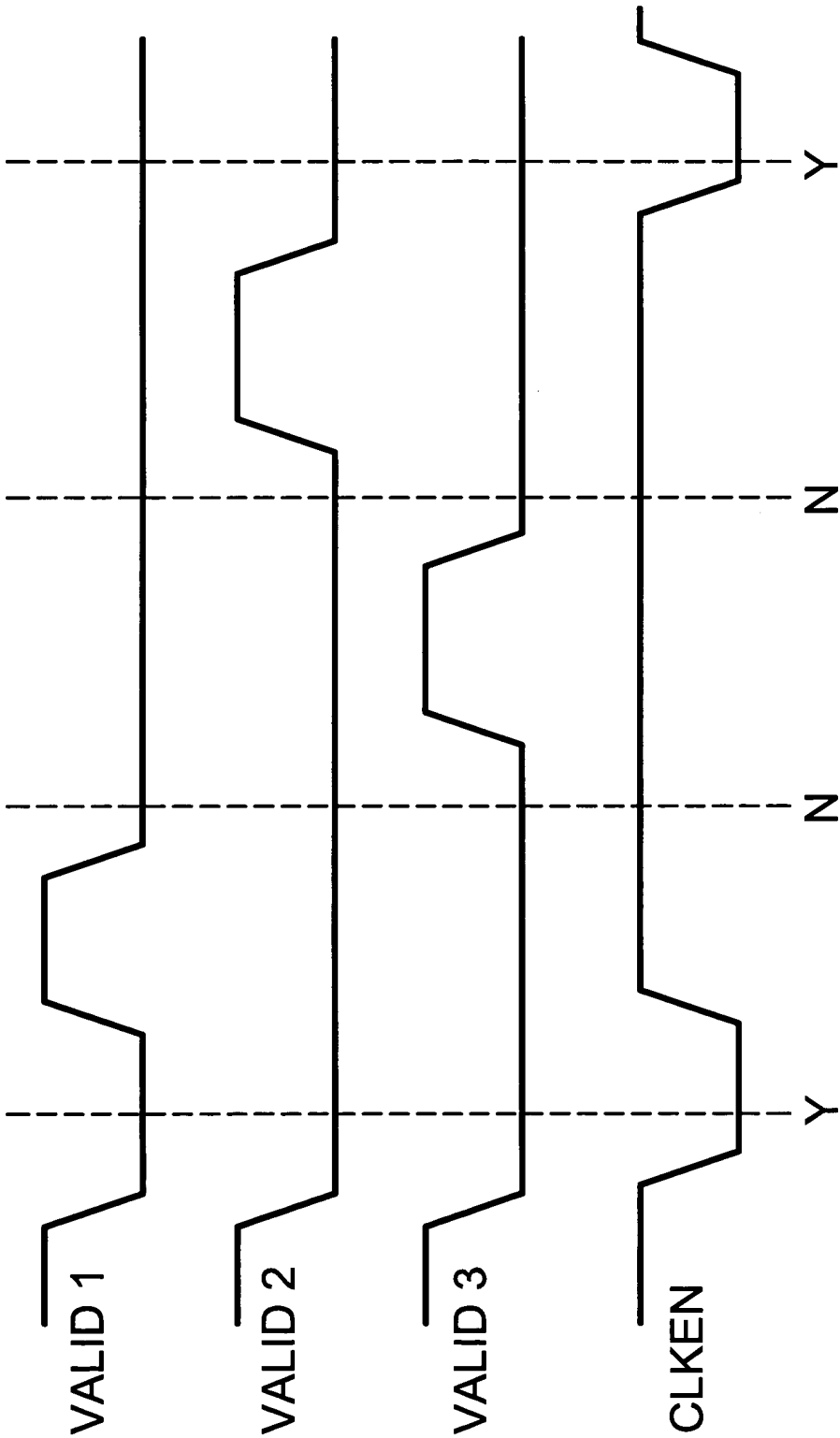


FIG. 10

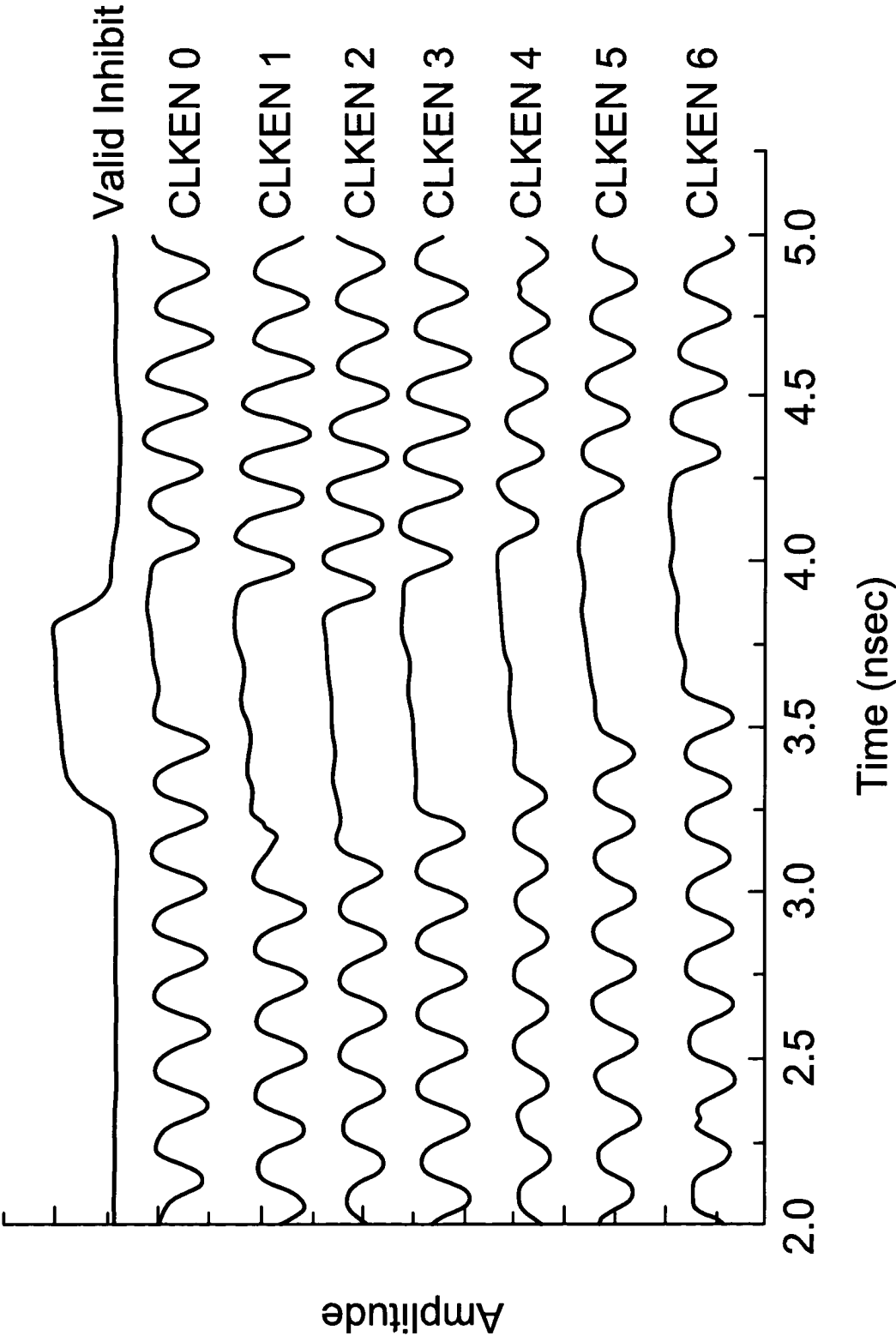
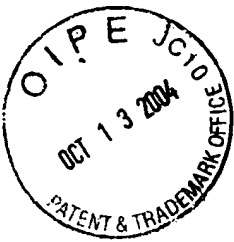


FIG. 11